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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Manjunath D. Haritsa et al.  
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FILING DATE: October 17, 2001  
TITLE: CLOCK SKEW VERIFICATION METHODOLOGY  
FOR GRID-BASED DESIGN  
EXAMINER: Tat, Binh C.  
ART UNIT: 2825

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**RESPONSE TO OFFICE ACTION**

COMMISSIONER FOR PATENTS  
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Dear Sir:

This paper is in response to the Office Action dated July, 14, 2003. Please consider the  
following.

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OCT - 6 2003  
TECHNOLOGY CENTER 2800